

AMENDMENTS

IN THE CLAIMS:

Claims 1-9 (Cancelled)

Claim 10. (Currently Amended) A sealed semiconductor device comprising:
a semiconductor chip;
a lead frame including internal leads, a plurality of said internal leads extending
across part of and spaced from a surface of said semiconductor chip; and
a die pad separate from and not ~~continuously~~ connected to said lead frame and on
which said semiconductor chip is mounted, wherein ~~said lead frame includes protrusions~~
extending at least one of said internal leads extends substantially perpendicular to and
~~contacting contacts~~ said die pad.

Claim 11. (Currently Amended) A sealed semiconductor device comprising:
a semiconductor chip;
a lead frame including internal leads extending across part of and spaced from a
surface of said semiconductor chip, and
a die pad separate from and not ~~continuously~~ connected to said lead frame and on
which said semiconductor chip is mounted, said die pad including fixed protrusions
extending toward and contacting some of said internal leads.

Claim 12-16 (Cancelled)

Claim 17. (Currently Amended) The sealed semiconductor device according to claim
10, wherein ~~said protrusions are~~ at least one internal lead perpendicular to and contacting said
die pad is peripheral to and does not contact said semiconductor chip.

Claim 18. (Currently Amended) The lead frame according to claim 10, wherein said
die pad is substantially rectangular and includes a pair of longer sides and a pair of shorter
sides and ~~said protrusions extend~~ at least one internal lead perpendicular to and contacting
said die pad extends proximate the pair of longer sides of said die pad.

Claim 19. (Currently Amended) The lead frame according to claim 10, wherein said
die pad is substantially rectangular and includes a pair of longer sides and a pair of shorter

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sides and said ~~protrusions extend~~ at least one internal lead perpendicular to and contacting said die pad extends proximate the pair of shorter sides of said die pad.

Claim 20. (Previously Amended) The sealed semiconductor device according to claim 21, further comprising a die pad on which said semiconductor chip is mounted.

Claim 21. (Currently Amended) A sealed semiconductor device comprising:
a semiconductor chip;
a lead frame including internal leads extending across part of and spaced from a surface of said semiconductor chip; and
a tape including four tape members located at respective corners of said semiconductor chip, each tape member being disposed between said semiconductor chip and some of said internal leads, holding said semiconductor chip and said internal leads at a fixed distance from each other, each of said tape members having a first surface to which some of said internal leads are bonded and fixed, and a second surface, in contact with but not fixed adhered to the surface of said semiconductor chip, only a portion of the second surface of each of said tape members contacting the surface of said semiconductor chip.

Claim 22. (Currently Amended) A lead frame and tape for a sealed semiconductor device having a rectangular semiconductor chip sealed within an encapsulating resin, said lead frame and tape comprising:

internal leads extending toward and electrically connected with wires to respective pads located approximately along a central axis of the semiconductor chip; and
a tape including four tape members, each tape member having a first surface to which some of said internal leads are fixed, each of said tape members being arranged at a respective corner of the semiconductor chip so that only a portion of a second surface of each of said tape members contacts but is not adhered to a surface of the semiconductor chip when the semiconductor chip is sealed within the encapsulating resin.